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**Universal serial bus interfaces for data and power -  
Part 3: USB battery charging specification, Revision 1.2  
(IEC 62680-3:2013)**

Interfaces de bus universel en série pour  
les données et l'alimentation électrique -  
Partie 3: Spécification de chargement des  
batteries USB, révision 1.2  
(CEI 62680-3:2013)

Schnittstellen des Universellen Seriellen  
Busses für Daten und Energie -  
Teil 3: Festlegung für den USB-Batterie-  
Ladevorgang, Überarbeitung 1.2  
(IEC 62680-3:2013)

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## Foreword

The text of document 100/2157/FDIS, future edition 1 of IEC 62680-3, prepared by Technical Area 14 "Interfaces and methods of measurement for personal computing equipment" of IEC/TC 100 "Audio, video and multimedia systems and equipment" was submitted to the IEC-CENELEC parallel vote and approved by CENELEC as EN 62680-3:2013.

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# INTERNATIONAL STANDARD

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**Universal serial bus interfaces for data and power –  
Part 3: USB Battery Charging Specification, Revision 1.2**

**Interfaces de bus universel en série pour les données et l'alimentation  
électrique –  
Partie 3: Spécification de chargement des batteries USB, révision 1.2**





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# INTERNATIONAL STANDARD

# NORME INTERNATIONALE



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**Universal serial bus interfaces for data and power –  
Part 3: USB Battery Charging Specification, Revision 1.2**

**Interfaces de bus universel en série pour les données et l'alimentation  
électrique –  
Partie 3: Spécification de chargement des batteries USB, révision 1.2**

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**UNIVERSAL SERIAL BUS INTERFACES FOR DATA AND POWER –****Part 3: USB Battery Charging Specification, Revision 1.2**

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The text of this standard is based on documents prepared by the USB Implementers Forum (USB-IF). The structure and editorial rules used in this publication reflect the practice of the organization which submitted it.

The text of this standard is based on the following documents:

FDIS	Report on voting
100/2157/FDIS	100/2190/RVD

Full information on the voting for the approval of this standard can be found in the report on voting indicated in the above table.

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IEC 62680-2, *Universal Serial Bus interfaces for data and power - Part 2: USB Micro-USB Cables and Connectors Specification, Revision 1.01*

IEC 62680-3, *Universal Serial Bus interfaces for data and power - Part 3: USB Battery Charging Specification, Revision 1.2*

IEC 62680-4, *Universal Serial Bus interfaces for data and power - Part 4: Universal Serial Bus Cables and Connectors Class Document Revision. 2.0*

This part of the IEC 62680 series consists of several distinct parts:

- the main body of the text, which consists of the original specification and all ECN and Errata developed by the USB-IF;

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**Battery Charging  
Specification  
(Including errata and ECNs through March 15, 2012)**

**Revision 1.2  
March 15, 2012**

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**Revision History**

<b>Revision</b>	<b>Date</b>	<b>Author</b>	<b>Description</b>
BC1.0	Mar 8, 2007	Terry Remple	First release
BC1.1	April 15, 2009	Terry Remple	Major updates to all sections. Added Data Contact Detect protocol, and Accessory Charger Adapter.
BC1.2	Oct 5, 2010	Terry Remple Adam Burns	<p>Following items indicate changes from BC1.1 to BC1.2. References below to Section, Figures and Tables refer to BC1.2, unless BC1.1 is specifically indicated.</p> <ol style="list-style-type: none"> <li>1. Allow DCPs to output more than 1.5A. Allows Portable Devices (PDs) with switch mode chargers to draw more power. Section 4.4.1.</li> <li>2. Increase minimum CDP current to 1.5A. Without change, PDs had to draw less than 500mA, to avoid CDP shutdown. Table 5-2.</li> <li>3. Indicate that ICDP max and IDCP max limits of 5A come from USB 2.0, and are safety limits. Table 5-2 note 1.</li> <li>4. Allow PDs to draw up to 1.5A during HS chirp and traffic. Remove previous limits of 560mA and 900mA which was based on HS common mode ranges. Section 3.5.</li> <li>5. Require CDPs to support 1.5A during HS chirp and traffic. Affects CDP common mode range. Section 3.5.</li> <li>6. Reduce maximum PD current from 1.8A to 1.5A, to avoid shutdown when attached to CDP. Table 5-2.</li> <li>7. Rename Docking Station to ACA-Dock, to avoid confusion with other types of Docking Stations.</li> <li>8. Require ACA-Dock to differentiate itself from an ACA, by enabling VDM_SRC during no activity. Section 3.2.4.4.</li> <li>9. Allow CDP to leave VDM_SRC enabled while peripheral not connected. Section 3.2.4.2.</li> <li>10. Remove ICHG_SHTDWN. This was a recommended max output current for Charging Ports with VBUS grounded. BC1.1 Section 4.1.</li> <li>11. Require VDP_SRC to not pull D+ below 2.2V when D+ is being pulled to VDP_UP through RDP_UP. Require VDM_SRC to not pull D- below 2.2V when D- is being pulled high. Required for ACA-Dock support. Table 5-1 notes 1 and 2.</li> <li>12. Make DCD current source optional for PDs. Section 3.2.3.</li> <li>13. Make DCD timeout required for PDs. Section 3.2.3.</li> <li>14. Make Secondary Detection optional for PDs. Section 4.6.2.</li> <li>15. Make Good Battery Algorithm required behavior for PDs. Section 3.2.4.</li> <li>16. Remove resistive detection. BC1.1 Section 3.9.</li> <li>17. Change PD Required Operating Range to include 4.5V at 500mA. Figure 4-3.</li> <li>18. Allow any downstream port to act as a DCP. Section 4.1.3.</li> <li>19. Require PDs to enable VDP_SRC or RDP_PU when charging from a DCP. Section 3.3.2.</li> <li>20. Allow chargers to renegotiate current with PD by dropping and reasserting VBUS. Section 4.1.3.</li> </ol>

Revision	Date	Author	Description
			<p>21. Require PDs to discharge their own VBUS input after VBUS drops to support charger port renegotiation request. Section 4.6.3.</p> <p>22. Allow PDs to disconnect and repeat Charger Detection multiple times while attached, with specified timing. Section 4.6.3.</p> <p>23. Reduce DCP input impedance between D+, D- to VBUS and ground from 1M<math>\Omega</math> to 300k<math>\Omega</math>. Section 4.4.3.</p> <p>24. Require CDPs to recover after over-current condition. Section 4.2.2.</p> <p>25. Allow greater DCP undershoot for large load current steps, to enable low quiescent current chargers required by Europe. Section 4.4.2.</p> <p>26. Define ACAs and ACA-Docks as types of Charging Ports. Section 1.4.5.</p> <p>27. Use session valid voltage range defined in EH and OTG Supplement rev 2.0. Section 3.2.2.</p> <p>28. Only devices that can operate stand-alone from internal battery power are allowed to use the Dead Battery Provision. Section 2.2.</p> <p>29. Allow compound PDs to draw ISUSP plus an responsible for protecting themselves against higher voltages on VBUS. BC1.1 Section 6.7.</p> <p>45. Require ACAs to continue providing power to OTG device from Charging Port, even if ground offsets or USB reset cause D- to go below VDAT_REF. Section 6.2.6.</p> <p>46. Change charger shutdown recovery time (TSHTDWN_REC) from 2 seconds to 2 minutes. Table 5-5.</p> <p>47. Indicate that ACA-Dock is required to pull D+ to VDP_UP with RDP_UP when VBUS is asserted. Section 3.2.4.4.</p> <p>48. Remove statements regarding devices with multiple receptacles. Covered in Multiple Receptacle white paper at <a href="http://www.usb.org/developers/docs/">http://www.usb.org/developers/docs/</a>.</p> <p>49. Improve readability by adding and updating drawings, re-structuring sections, and clarifying text.</p>
BC 1.2 plus errata	Oct 12, 2011	Pat Crowe	Includes errata changes from Oct 12, 2011
BC 1.2 plus further errata	Mar 15, 2012	Pat Crowe	Includes errata changes from Mar 15, 2012: <ol style="list-style-type: none"> <li>1. Corrections to Micro ACA specification.</li> </ol>

## Acronyms

ACA	Accessory Charger Adapter
CDP	Charging Downstream Port
DBP	Dead Battery Provision
DCD	Data Contact Detect
DCP	Dedicated Charging Port
FS	Full Speed
HS	High-Speed
LS	Low-Speed
OTG	On-The-Go
PC	Personal Computer
PD	Portable Device
PHY	Physical Layer Interface for High-Speed USB
PS2	Personal System 2
SDP	Standard Downstream Port
SRP	Session Request Protocol
TPL	Targeted Peripheral List
USB	Universal Serial Bus
USBCV	USB Command Verifier
USB-IF	USB Implementers Forum
VBUS	Voltage line of the USB interface



## 1 Introduction

### 1.1 Scope

The Battery Charging Working Group is chartered with creating specifications that define limits as well as detection, control and reporting mechanisms to permit devices to draw current in excess of the USB 2.0 specification for charging and/or powering up from dedicated chargers, hosts, hubs and charging downstream ports. These mechanisms are backward compatible with USB 2.0 compliant hosts and peripherals.

**koniec náhľadu – text ďalej pokračuje v platenej verzii STN**