

<b>STN</b>	<b>Skúšobné metódy na elektrotechnické materiály, dosky s plošnými spojmi a iné spájacie štruktúry a zostavy. Časť 2-719: Skúšobné metódy na materiály na spájacie štruktúry. Relatívna permitivita a stratový činiteľ (500 MHz až 10 GHz).</b>	<b>STN EN 61189-2-719</b>
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Test methods for electrical materials, printed boards and other interconnection structures and assemblies - Part 2-719: Test methods for materials for interconnection structures - Relative permittivity and loss tangent (500 MHz to 10 GHz)

Táto norma obsahuje anglickú verziu európskej normy.  
This standard includes the English version of the European Standard.

Táto norma bola oznámená vo Vestníku ÚNMS SR č. 03/17

Obsahuje: EN 61189-2-719:2016, IEC 61189-2-719:2016

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Úrad pre normalizáciu, metrológiu a skúšobníctvo Slovenskej republiky, 2017

Podľa zákona č. 264/1999 Z. z. o technických požiadavkách na výrobky a o posudzovaní zhody a o zmene a doplnení niektorých zákonov v znení neskorších predpisov sa slovenská technická norma a časti slovenskej technickej normy môžu rozmnogoovať alebo rozširovať len so súhlasom slovenského národného normalizačného orgánu.

**EUROPEAN STANDARD**  
**NORME EUROPÉENNE**  
**EUROPÄISCHE NORM**

**EN 61189-2-719**

October 2016

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English Version

**Test methods for electrical materials, printed boards and other  
interconnection structures and assemblies -  
Part 2-719: Test methods for materials for interconnection  
structures - Relative permittivity and loss tangent (500 MHz to 10  
GHz)**  
**(IEC 61189-2-719:2016)**

Méthode d'essai pour les matériaux électriques, les cartes  
imprimées et autres structures d'interconnexion et  
ensembles - Partie 2-719: Méthodes d'essai des matériaux  
pour structures d'interconnexion - Permittivité relative et  
tangente de perte (500 MHz à 10 GHz)  
(IEC 61189-2-719:2016)

Prüfverfahren für Elektromaterialien, Leiterplatten und  
andere Verbindungsstrukturen und Baugruppen -  
Teil 2-719: Prüfverfahren für Materialien von  
Verbindungsstrukturen - Relative Permittivität und  
Verlustfaktor (500 MHz bis 10 GHz)  
(IEC 61189-2-719:2016)

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European Committee for Electrotechnical Standardization  
Comité Européen de Normalisation Electrotechnique  
Europäisches Komitee für Elektrotechnische Normung

**CEN-CENELEC Management Centre: Avenue Marnix 17, B-1000 Brussels**

**European foreword**

The text of document 91/1366/FDIS, future edition 1 of IEC 61189-2-719, prepared by IEC/TC 91 "Electronics assembly technology" was submitted to the IEC-CENELEC parallel vote and approved by CENELEC as EN 61189-2-719:2016.

The following dates are fixed:

- latest date by which the document has to be implemented at national level by publication of an identical national standard or by endorsement (dop) 2017-05-16
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(normative)**Normative references to international publications  
with their corresponding European publications**

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<u>Publication</u>	<u>Year</u>	<u>Title</u>	<u>EN/HD</u>	<u>Year</u>
IEC 60194	-	Printed board design, manufacture and assembly - Terms and definitions	-	-



# INTERNATIONAL STANDARD

# NORME INTERNATIONALE



**Test methods for electrical materials, printed boards and other interconnection structures and assemblies –**

**Part 2-719: Test methods for materials for interconnection structures – Relative permittivity and loss tangent (500 MHz to 10 GHz)**

**Méthode d'essai pour les matériaux électriques, les cartes imprimées et autres structures d'interconnexion et ensembles –**

**Partie 2-719: Méthodes d'essai des matériaux pour structures d'interconnexion – Permittivité relative et tangente de perte (500 MHz à 10 GHz)**





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# INTERNATIONAL STANDARD

# NORME INTERNATIONALE



**Test methods for electrical materials, printed boards and other interconnection structures and assemblies –**

**Part 2-719: Test methods for materials for interconnection structures – Relative permittivity and loss tangent (500 MHz to 10 GHz)**

**Méthode d'essai pour les matériaux électriques, les cartes imprimées et autres structures d'interconnexion et ensembles –**

**Partie 2-719: Méthodes d'essai des matériaux pour structures d'interconnexion – Permittivité relative et tangente de perte (500 MHz à 10 GHz)**

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# INTERNATIONAL ELECTROTECHNICAL COMMISSION

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## **TEST METHODS FOR ELECTRICAL MATERIALS, PRINTED BOARDS AND OTHER INTERCONNECTION STRUCTURES AND ASSEMBLIES –**

### **Part 2-719: Test methods for materials for interconnection structures – Relative permittivity and loss tangent (500 MHz to 10 GHz)**

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The text of this standard is based on the following documents:

FDIS	Report on voting
91/1366/FDIS	91/1380/RVD

Full information on the voting for the approval of this standard can be found in the report on voting indicated in the above table.

This publication has been drafted in accordance with the ISO/IEC Directives, Part 2.

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**TEST METHODS FOR ELECTRICAL MATERIALS, PRINTED BOARDS AND OTHER INTERCONNECTION STRUCTURES AND ASSEMBLIES –****Part 2-719: Test methods for materials for interconnection structures – Relative permittivity and loss tangent (500 MHz to 10 GHz)****1 Scope**

This part of IEC 61189 specifies a test method of relative permittivity and loss tangent of printed board and assembly materials, expected to be determined 2 to 10 of relative permittivity and 0,001 to 0,050 of loss tangent at 500 MHz to 10 GHz.

**2 Normative references**

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IEC 60194, *Printed board design, manufacture and assembly – Terms and definitions*

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