

STN	Zabezpečovanie výrobkov kozmického programu Vysoko pevné spájkovanie pre povrchovú montáž a rozličné technológie	STN EN 16602-70-38 31 0542
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Space product assurance - High-reliability soldering for surface-mount and mixed technology

Táto norma obsahuje anglickú verziu európskej normy.
This standard includes the English version of the European Standard.

Táto norma bola oznámená vo Vestníku ÚNMS SR č. 09/19

Obsahuje: EN 16602-70-38:2019

129074

EUROPEAN STANDARD**EN 16602-70-38****NORME EUROPÉENNE****EUROPÄISCHE NORM**

April 2019

ICS 25.160.50; 49.140

English version

Space product assurance - High-reliability soldering for surface-mount and mixed technology

Assurance produit des projets spatiaux - Soudure
haute fiabilité pour technologies à montage de surface
et mixte

Raumfahrtproduktsicherung - Hochzuverlässiges
Löten von Oberflächen-Befestigungen und gemischte
Technologien

This European Standard was approved by CEN on 5 October 2018.

CEN and CENELEC members are bound to comply with the CEN/CENELEC Internal Regulations which stipulate the conditions for giving this European Standard the status of a national standard without any alteration. Up-to-date lists and bibliographical references concerning such national standards may be obtained on application to the CEN-CENELEC Management Centre or to any CEN and CENELEC member.

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European Foreword

This document (EN 16602-70-38:2019) has been prepared by Technical Committee CEN-CENELEC/TC 5 "Space", the secretariat of which is held by DIN.

This standard (EN 16602-70-38:2019) originates from ECSS-Q-ST-70-38C Rev.1.

This European Standard shall be given the status of a national standard, either by publication of an identical text or by endorsement, at the latest by October 2019, and conflicting national standards shall be withdrawn at the latest by October 2019.

Attention is drawn to the possibility that some of the elements of this document may be the subject of patent rights. CEN [and/or CENELEC] shall not be held responsible for identifying any or all such patent rights.




This document has been prepared under a standardization request given to CEN by the European Commission and the European Free Trade Association.

This document has been developed to cover specifically space systems and has therefore precedence over any EN covering the same scope but with a wider domain of applicability (e.g. : aerospace).

According to the CEN-CENELEC Internal Regulations, the national standards organizations of the following countries are bound to implement this European Standard: Austria, Belgium, Bulgaria, Croatia, Cyprus, Czech Republic, Denmark, Estonia, Finland, Former Yugoslav Republic of Macedonia, France, Germany, Greece, Hungary, Iceland, Ireland, Italy, Latvia, Lithuania, Luxembourg, Malta, Netherlands, Norway, Poland, Portugal, Romania, Serbia, Slovakia, Slovenia, Spain, Sweden, Switzerland, Turkey and the United Kingdom.

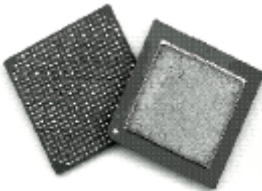


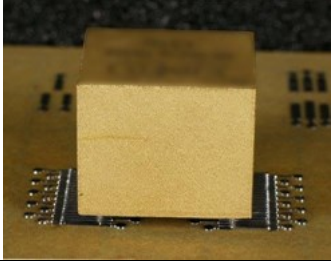
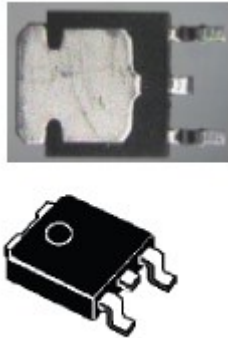
Introduction

This Standard prescribes requirements for electrical connections of leadless and leaded surface mounted devices (SMD) on spacecraft and associated equipment, utilising a range of substrate assemblies and employing solder as the interconnection media. The principal types of SMDs can be gathered in the following families:

<p>Rectangular and square end-capped or end-metallized device with rectangular body, leadless chip (see 11.5.2) e.g. end capped chip resistors and end capped chip capacitors.</p>	
<p>Cylindrical and square end-capped devices with cylindrical body, leadless chip (see 11.5.4) e.g. MELF for cylindrical end capped or e.g. D-5A for square end capped</p>	
<p>Bottom terminated chip device (see 11.5.3) This type of device has metallised terminations on the bottom side only. e.g. inductors and SMD0.5, SMD1, SMD2, SMD0.2, SMD0.22 e.g. Quad Flat Pack No lead (QFN)</p>	

<p>Castellated chip carrier device (see 11.5.5)</p> <p>The main device of this type is leadless ceramic chip carrier (LCCC).</p> <p>e.g. LCC6</p>	
<p>Flat pack and gull-wing leaded device with round, rectangular, ribbon leads (see 11.5.6)</p> <p>e.g. small-outline transistor (SOT), small-outline package (SO), flat pack and quad flat pack (QFP) and SMD connectors with stress-relief.</p> <p>This family also comprises devices for through-hole mounting that have been reconfigured to surface mounting.</p>	
<p>Moulded magnetics (see 11.5.13)</p> <p>e.g. 1553 interface transformers or specific transformers</p>	
<p>“J” leaded device (see 11.5.7)</p> <p>e.g. ceramic leaded chip carriers (CLCC) and plastic leaded chip carriers (PLCC).</p>	

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<p>Area array devices (AAD) (see 11.5.8)</p> <p>The interconnections between solder footprints on the devices and solder footprints on the PCB consist entirely of solder.</p> <p>The devices have either solder balls (Ball Grid Array - BGA) or solder columns (Column Grid Array - CGA) applied to the solder footprints on the devices prior to mounting on a PCB (normally done by the device manufacturer). The solder balls on the BGAs can consist of either eutectic solder or high temperature solder (5 - 10 % Sn) whereas the solder columns on the CGAs always consist of high temperature solder. Although BGAs are usually presented as a device family, there exist a large number of BGA devices with wide-ranging properties. The vast majority of BGA devices are non hermetic.</p>	
<p>Devices with ribbon terminals without stress relief (flat lug leads) (see 11.5.9)</p> <p>This package has flat leads extending from the sides</p>	
<p>Device with Inward formed L-shaped leads (see 11.5.10) e.g. moulded tantalum chip capacitors.</p>	
<p>Stacked modules devices with leads protruding vertically from bottom (see 11.5.11)</p>	
<p>Leaded device with plane termination (see 11.5.12) e.g. Diode PAcKage (DPAK or TO252)</p>	 <p style="text-align: center;">DPAK</p>

1 Scope

This Standard defines the technical requirements and quality assurance provisions for the manufacture and verification of high-reliability electronic circuits based on surface mounted device (SMD) and mixed technology.

The Standard defines acceptance and rejection criteria for high-reliability manufacture of surface-mount and mixed-technology circuit assemblies intended to withstand normal terrestrial conditions and the vibrational g-loads and environment imposed by space flight.

The proper tools, correct materials, design and workmanship are covered by this document. Workmanship standards are included to permit discrimination between proper and improper work.

The assembly of leaded devices to through-hole terminations and general soldering principles are covered in ECSS-Q-ST-70-08.

Requirements related to printed circuit boards are contained in ECSS-Q-ST-70-10, ECSS-Q-ST-70-11 and ECSS-Q-ST-70-12. The mounting and supporting of devices, terminals and conductors prescribed herein applies to assemblies at PCB level designed to continuously operate over the mission within the temperature limits of -55 °C to +85 °C.

For temperatures outside this normal range, special design, verification and qualification testing is performed to ensure the necessary environmental survival capability.

Special thermal heat sinks are applied to devices having high thermal dissipation (e.g. junction temperatures of 110 °C, power transistors) in order to ensure that solder joints do not exceed 85 °C.

Verification of SMD assembly processes is made on test vehicles (surface mount verification samples). Temperature cycling ensures the operational lifetime for spacecraft. However, mechanical testing only indicates SMD reliability as it is unlikely that the test vehicle represents every flight configuration.

This Standard does not cover the qualification and acceptance of the EQM and FM equipment with surface-mount and mixed-technology.

The qualification and acceptance tests of equipment manufactured in accordance with this Standard are covered by ECSS-E-ST-10-03.

This standard may be tailored for the specific characteristics and constraints of a space project, in accordance with ECSS-S-ST-00.

2**Normative references**

The following normative documents contain provisions which, through reference in this text, constitute provisions of this ECSS Standard. For dated references, subsequent amendments to, or revisions of any of these publications do not apply. However, parties to agreements based on this ECSS Standard are encouraged to investigate the possibility of applying the most recent editions of the normative documents indicated below. For undated references the latest edition of the publication referred to applies.

EN reference	Reference in text	Title
EN 16601-00-01	ECSS-S-ST-00-01	ECSS system – Glossary of terms
EN 16601-40	ECSS-M-ST-40	Space project management – Configuration and information management
EN 16602-10-09	ECSS-Q-ST-10-09	Space product assurance – Nonconformance control system
EN 16602-20	ECSS-Q-ST-20	Space product assurance – Quality assurance
EN 16602-60	ECSS-Q-ST-60	Space product assurance – Electrical, electronic and electromechanical (EEE) components
EN 16602-60-05	ECSS-Q-ST-60-05	Space product assurance – Generic requirements for hybrids
EN 16602-60-13	ECSS-Q-ST-60-13	Space product assurance - Commercial electrical, electronic and electromechanical (EEE) components
EN 16602-70	ECSS-Q-ST-70	Space product assurance – Materials, mechanical parts and processes
EN 16602-70-01	ECSS-Q-ST-70-01	Space product assurance – Cleanliness and contamination control
EN 16602-70-02	ECSS-Q-ST-70-02	Space product assurance – Thermal vacuum outgassing test for the screening of space materials
EN 16602-70-08	ECSS-Q-ST-70-08	Space product assurance – Manual soldering of high-reliability electrical connections
EN 16602-70-10	ECSS-Q-ST-70-10	Space product assurance – Qualification of printed circuit boards
EN 16602-70-11	ECSS-Q-ST-70-11	Space product assurance – Procurement of printed circuit boards

EN reference	Reference in text	Title
EN 16602-70-12	ECSS-Q-ST-70-12	Space product assurance – Design rules for printed circuit boards
EN 16602-70-28	ECSS-Q-ST-70-28	Space product assurance – Repair and modification of printed circuit board assemblies for space use.
EN 16602-70-71	ECSS-Q-ST-70-71	Space product assurance – Materials processes and their data selection
	MIL-STD-883 Method 2009	Test Method Standard, Microcircuits
	IPC-TM-650: 2.6.3.3 Issue 2004	Test methods manual. Surface Insulation Resistance, Fluxes

koniec náhľadu – text ďalej pokračuje v platenej verzii STN